

**RF AND DIGITAL SIGNAL PROCESSING FOR
SOFTWARE-DEFINED RADIO: A MULTI-STANDARD
MULTI-MODE APPROACH**

Rhae Clingerman

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RF and Digital Signal Processing for. Software-Defined Radio. A Multi-Standard Multi-Mode Approach. Tony J. Roupheal. AMSTERDAM • BOSTON.

Software-Defined Radio FPGA Cores: Building towards a Domain-Specific Language

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Tony J Roupael Find more information about: The general structure of the design of the DSP cores is shown in Figure 2. By continuing you agree to the use of cookies.

TheE-mailAddressesfieldisrequired. Further benchmark tests for a Gbe core, FMC interface core, and the FM receiver application are performed and results shown in Table 1 show a satisfactory performance needed for SDR. Correspondence should be addressed to Lekhobola Tsoeunyane ; moc. From until present, Dr.

ThefirststepinvolvedconsultationwithSDRexperts.TheADCsamplingrate arrives at the input in a sequential order and output data leaves the core in a bit-reversed order. Signal processing -- Digital techniques.